# A.D.J. DHARMAMBAL POLYTECHNIC COLLEGE, NAGAPATTINAM

# (A Govt. Aided Co-Educational Institution)

# INTERNAL EXAMINATION – II February 2024

# **OVERALL TIME TABLE**

## INTERNAL EXAM CELL - CIRCULAR **Dated: 22.02.24**

I YEAR – II SEMESTER

Time: 2.30 pm to 4.30 pm

Date/Dept.	26.02.24	27.02.24	29.02.24	01.03.24
I YEAR	Batch A - Applied Mathematics Batch B -Tamilarum Thozirnutpamum Batch C - Communicative English	Basics of Engineering	Batch B - Communicative English	Batch A - Communicative English Batch B - Applied Mathematics Batch C - Tamilarum Thozirnutpamum

II YEAR-IV SEMESTER Time: 2.30 pm to				
Date/Dept.	26.02.24	27.02.24	28.02.24	01.03.24
ECE	Industrial Electronics	Communication	Analog and Digital	E-Vehicle Technology
		Engineering	Electronics	
CE	Relational Database	Object Oriented	Computer Architecture	Web Design and Programming
	Management System	Programming With Java		
ICE	Analog and Digital	Measurement of Process	Measurements and	E-Vehicle Technology
	Electronics	Variables	Instruments	
EEE	Electrical Machines - II	Measurements, Instruments	Analog and Digital	E-Vehicle Technology and
	Electrical Machines - II	and Transducers	Electronics	Policy
MECH	Manufacturing Technology II	Fluid Mechanics & Fluid	Electrical Drives and	Production and Quality
		Power	Controls	Management

III YEAR-V	Time: 2.30 pm to 4.30 pm		
Date/Dept.	27.02.24	28.02.24	29.02.24
ECE	Computer Hardware Servicing and Networking	Bio Medical Instrumentation	Embedded Systems
CE	Multimedia Systems	Computer Hardware and Servicing	Computer Network and Security
ICE	Industrial Automation and Drives	Power Plant Instrumentation	Bio Medical Instrumentation
EEE	Distribution And Utilization	Energy Conservation & Audit	Power Electronics

	COE					PRINCIPAL
Signature of HOD's						
	1. ECE	2. CE	3. ICE	4. EEE	5. MECH	6. I YEAR